

ABSTRACT OF THE DISCLOSURE

The present invention discloses a reset signal generating circuit including a power sensing stabilizing unit, a pull-up driving unit, a voltage adjusting unit, a feedback control unit, a pull-up control unit, a self pull-up bias unit and a self bias unit. The reset signal generating circuit generates a reset signal only when a power voltage reaches a predetermined level, regardless of a power-up slope. In addition, the reset signal generating circuit includes a temperature compensating circuit to compensate for variations of operation properties of the voltage adjusting unit due to variations of a temperature, thereby minimizing instability of generation of the reset signal due to variations of the temperature.